



Intel® VINETIC™ SVIP16 Codecs

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Essentials



 Export specifications

Product Collection	Intel® VINETIC™ Products
Code Name	Products formerly Belington
Status	Launched
Launch Date 	Q1'10
Recommended Customer Price 	-

Supplemental Information

Embedded Options Available 	No
Intel® VINETIC™ SVIP16 PEF33616 Codec is a 16-channel CODEC with intergrated DSP and linecard controller function, full wideband support and on-chip line testing with test head accuracy	

Package Specifications

Sockets Supported 	FCLBGA323
Low Halogen Options Available	See MDDS

More support options for Intel® VINETIC™ SVIP16 Codecs

 Product Support

 Downloads and Software

 Support Community

 Warranty and Replacement



Need more help?

Contact support



 Give Feedback

Intel classifications are for informational purposes only and consist of Export Control Classification Numbers (ECCN) and Harmonized Tariff Schedule (HTS) numbers. Any use made of Intel classifications are without recourse to Intel and shall not be construed as a representation or warranty regarding the proper ECCN or HTS. Your company as an importer and/or exporter is responsible for determining the correct classification of your transaction.

Refer to Datasheet for formal definitions of product properties and features.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

Some products can support AES New Instructions with a Processor Configuration update, in particular, i7-2630QM/i7-2635QM, i7-2670QM/i7-2675QM, i5-2430M/i5-2435M, i5-2410M/i5-2415M. Please contact OEM for the BIOS that includes the latest Processor configuration update.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

See <http://www.intel.com/content/www/us/en/architecture-and-technology/hyper-threading/hyper-threading-technology.html?wapkw=hyper+threading> for more information including details on which processors support Intel® HT Technology.

Max Turbo Frequency refers to the maximum single-core processor frequency that can be achieved with Intel® Turbo Boost Technology. See www.intel.com/technology/turboboost/ for more information.

The Recommended Customer Price ("RCP") is pricing guidance for Intel products. Prices are for direct Intel customers, typically represent 1,000-unit purchase quantities, and are subject to change without notice. Taxes and shipping, etc. not included. Prices may vary for other package types and shipment quantities, and special promotional arrangements may apply. If sold in bulk, price represents individual unit. Listing of these RCP does not constitute a formal pricing offer from Intel. Please work with your appropriate Intel representative to obtain a formal price quotation.

System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

Low Halogen: Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

For benchmarking data see <http://www.intel.com/performance>.

Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families. See <http://www.intel.com/content/www/us/en/processors/processor-numbers.html> for details.

Processors that support 64-bit computing on Intel® architecture require an Intel 64 architecture-enabled BIOS.



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