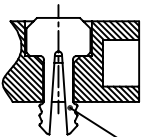
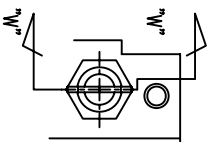




[illegible]

TYP.(2)
DOWN

92140-xxxxH

(OPTIONAL HOLD DOWN)

NOTE
1. THIS PRODUCT MEETS EUROPEAN UNION
DIRECTIVES AND OTHER COUNTRY
REGULATIONS AS DESCRIBED IN GS-22-008
2. PRODUCT SPEC: 110-263

mat'l. code					surface		tolerance		projection		product family	
					ISO 1302	ISO 406, ISO 1001				PCMCIA		
					tolerances unless otherwise specified							
litr	ecn	no	dr	date	angles	 MM				title		
AL	EX-A-011426	ZK	04/23/12		linear							
AM	EX-A-16386	JH	11/29/13		$\pm 2^\circ$	0.0XX \pm 0.051		scale		68 POS ELECTOR HEADER ASSY FOR 5.0V APPLICATION		
					dr	Mike Zhou				dwg no		
AH	MOS-0-19	MZ	11/14/06	engr	Mike Zhou	11/21/06				sheet 1 of 7 size		
AJ	MOS-0-06	SH	05/26/09	chr	David Qin	11/21/06				9214.0		
AK	EX-A-00749	ZK	11/20/11	apprd	Jack Wang	11/21/06				CUSTOMER		
sheet	revision	AM								Drawing		
index	sheet	1-7								A4		

92140-X X X X X X

— OPTIONAL LEED FREE

OPTIONAL HOLD DOWN

LOW COST VERSION

— SOLDER TAIL

0:R/A (FOR LOWER & SINGLE)

1 : SMT STAGGERED

2 : SMT IN LINE
3 : SMT IN LINE
(FOR DIPPER ONLY)

5: R/A

6: R/A (FOR LOWER & SINGLE, HOLD DOWN WITH HANGNAIL,

STAND OFF

0 : 0mm

1 : 4mm

2. 7

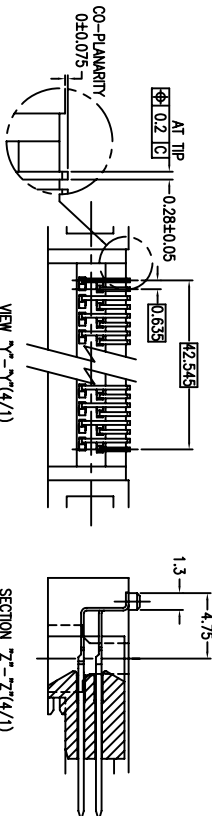
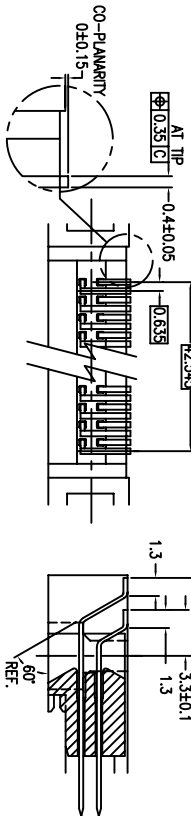
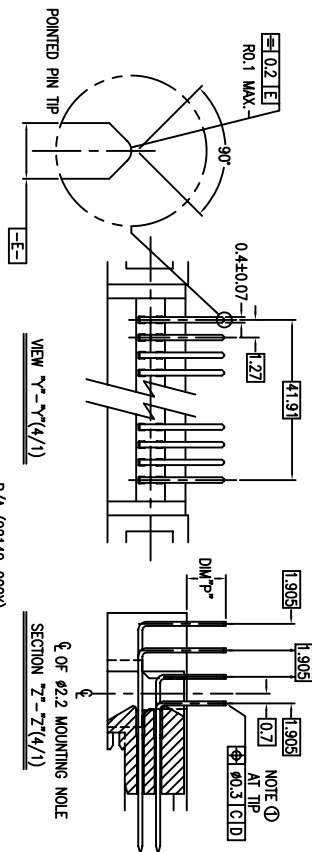
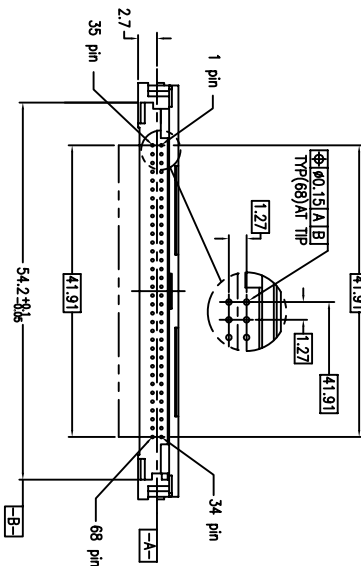
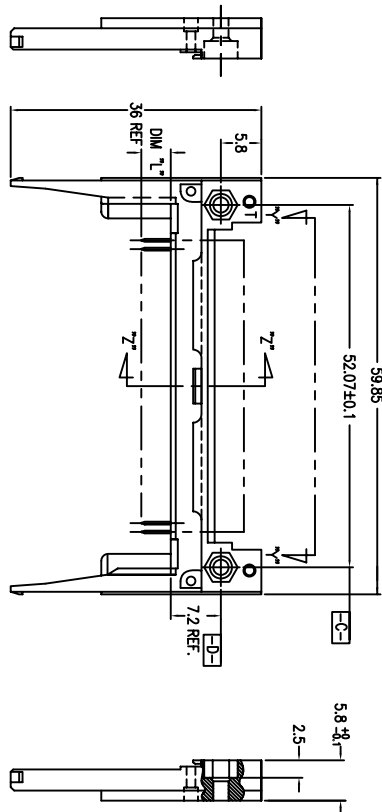
4 : 2mm

5 : 3mm

MOUNTING STYLE

0 : TOP MOUNTING

PRODUCT NO.
92140-000M
92140-000MLF
92140-000H
92140-000HLF
92140-001
92140-001LF
92140-001H
92140-001HLF
92140-002
92140-002LF
92140-002H
92140-002HLF



- NOTES
- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
 - SEE TA-946 FOR PCB LAYOUT.
 - MATERIAL
HOUSING : HIGH TEMPERATURE THERMOPLASTIC U94V-0 BLACK
PIN : COPPER ALLOY
HOLD-DOWN : BRASS
PIN
UNDER PLATING : 0.5um MIN. Ni,
CONTACT AREA : 0.076 um MIN. GOLD
 - FINISH

NOTES: SPECIAL VERSION: ONLY 0.25um Au
SOLDER AREA : 2.5um MIN. Sn-Pb,
OR 2.5um MIN. PURE Sn (FOR -XXXLF)
HOLD-DOWN : 2.5um MIN. PURE Sn.
5. RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
6. SEQUENCE PIN ASSIGNMENT

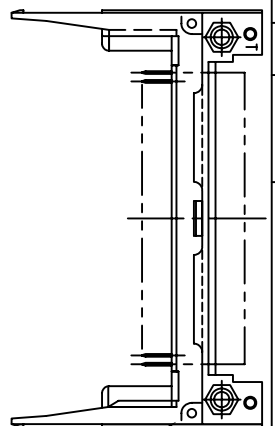
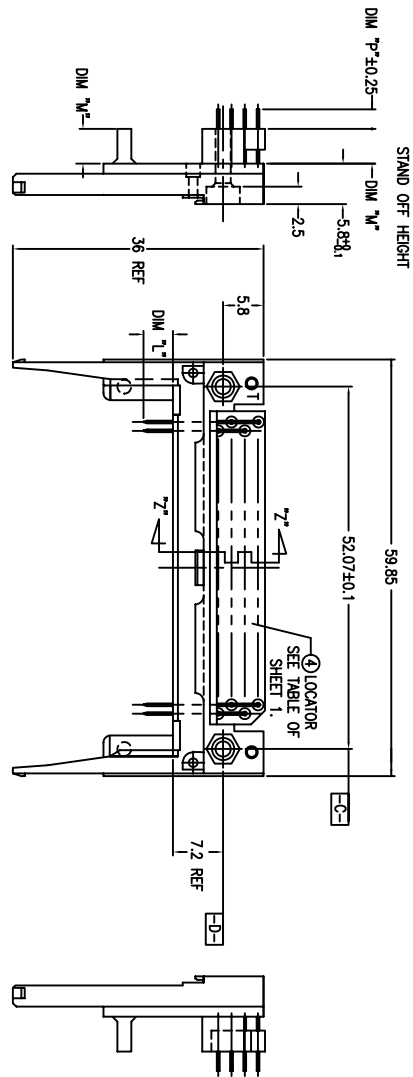
PIN 1
4.25±0.1
3.5±0.1
5.0±0.1
1.7±0.1
36.67
36.5±0.8

7. GENERAL TOLERANCE : ±0.3
8. SEE SHEET 1 REGARDING COMPONENTS.

mat'l. code	surface	tolerance	projection	product family
AM	ISO 1902	ISO 446 ISO 1901	MM	PCMCIA
dr	date	angles	linear	title
±2	0.0XX±0.051	0.0XX±0.3	0.0XX±0.13	68 POS ELECTOR HEADER ASSY FOR 5.0V APPLICATION
engr	Mike Zhou	11/21/06	11/21/06	dwg no
chr	David Qin	11/21/06	11/21/06	92140
appd	Jack Wang	11/21/06	11/21/06	sheet 2 of 7 size
type	CUSTOMER	Drawing	A4	

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PRODUCT NO.	PRODUCT NO.
92140-010	92140-010LF
92140-010H	92140-010HLF
92140-020	92140-020LF
92140-020H	92140-020HLF
92140-040	92140-040LF
92140-050	92140-050LF
92140-050H	92140-050HLF
92140-020MLF	92140-040HLF
92140-021MLF	
92140-026HLF	



ONLY FOR 92140-020MCLF / 021MCLF (WITHOUT RIB)

NOTE ①

0.3.3

1

3

□

1

(5/1)

KN ITSELF.

ACTIO III

1

10

•

1111

PMLIA

HEAT

APPLICA

sheet

[illegible]

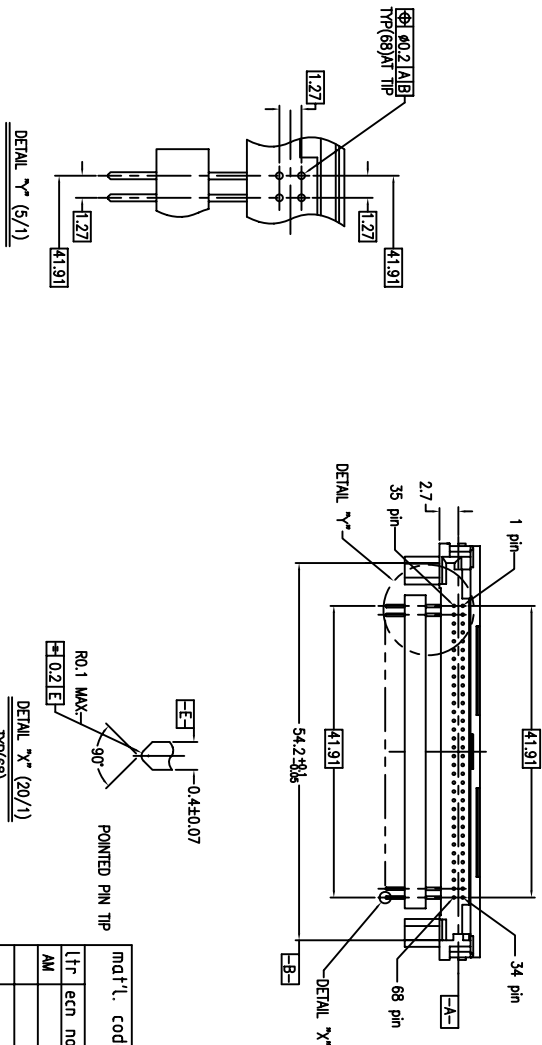
3
PDS: Bay - AM

4 | STATIS-Released Printed: Dec 06 2013

4 | Printed: Dec 06 2013

5

9



NOTES

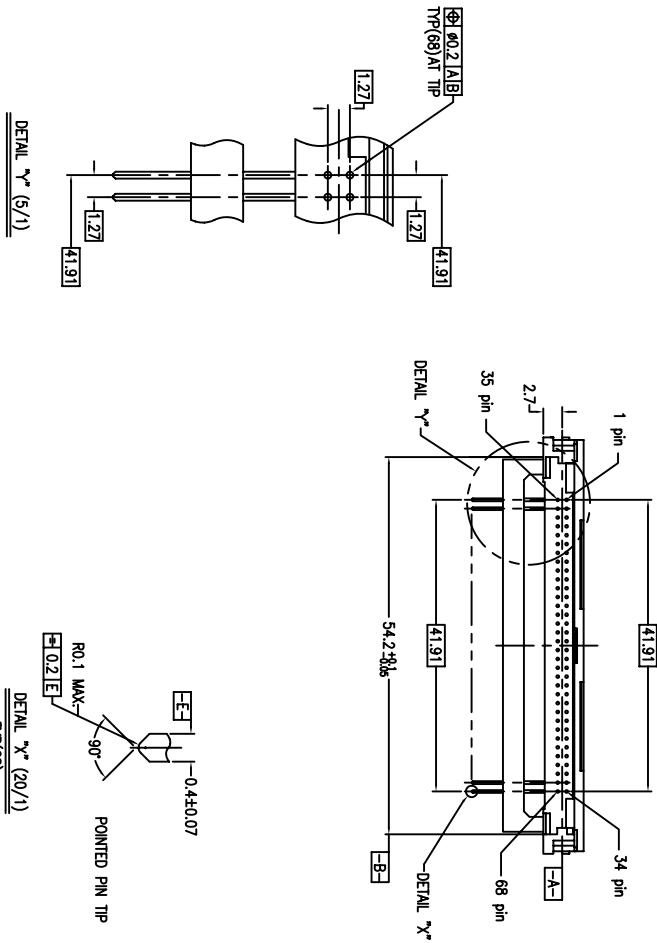
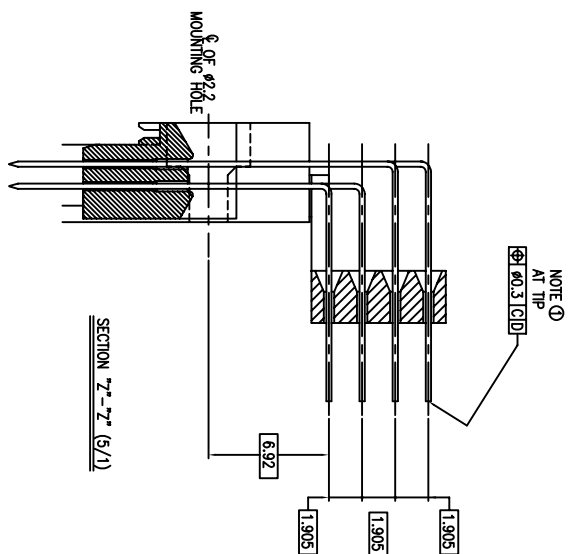
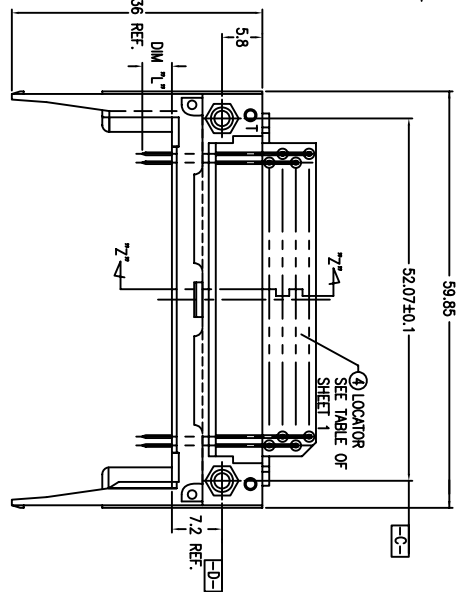
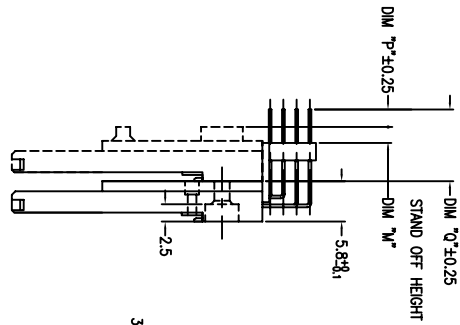
1. TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
2. SEE 1A-946 FOR PCB LAYOUT.
3. MATERIAL
HOLDING : HIGH TEMPERATURE THERMOPLASTIC U94V-0 BLACK
PIN : COPPER ALLOY
HOLD-DOWN : BRASS
FINISH : ENAMEL

1. UNDER PLATING : 0.5µm MIN. Ni.
 CONTACT AREA 1 : 0.076 µm MIN. GOLD
 CONTACT AREA 2 : 0.076 µm MIN. GOLD (FOR -XXXXLFL).
 SOLDER AREA : 2.5µm MIN. Sn-Pb.
 OR 2.5µm MIN. PURE Sn. (FOR -XXXXLFL&XXXXLFL)
 HOLD-DOWN : 2.5µm MIN. PURE Sn.
 5. RECOMMENDED PGB HOLE (OPTIONAL HOLD-DOWN) : 2.3x0.1
 6. SEQUENCE PIN ASSIGNMENT

	DIM. T ₁	
	4.25±0.1	5.0±0.1
PIN No.	OTHERS	36.67 36.57±0.08

	DMA T_g	
	4.25±0.1	5.0±0.1
PIN No.	OTHERS	36.67
		1.1734
		35.5168

mat'l. code		surface		tolerance		projection		product family		PCMCIA	
ISO 1302		ISO 406		ISO 1001				68 POS EJECTOR HEADER ASSY FOR 5.0V APPLICATION			
litr		ecr		no		date					
AM		angles		linear		0.±0.3		MM		title	
		±2°		0.XXX±0.051		scale		dwg no		sheet 3 of 7	
		dr		Mike Zhou		11/21/06		9214.0		size A4	
		engr		Mike Zhou		11/21/06					
		chr		David Qin		11/21/06					
		appd		Jack Wang		11/21/06					
sheet		revision						type		CUSTOMER	
index		sheet						drawing			



NOTES

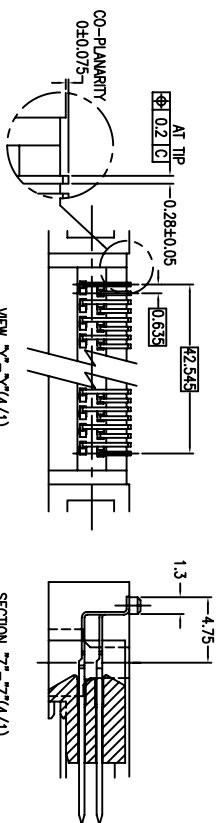
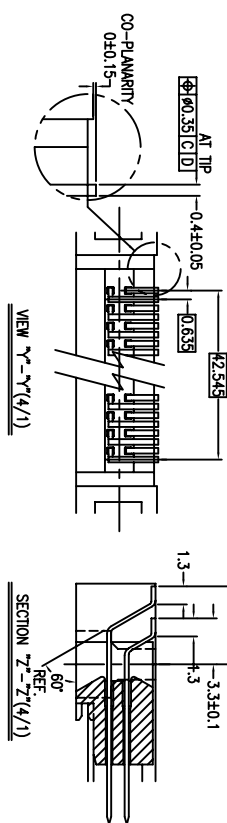
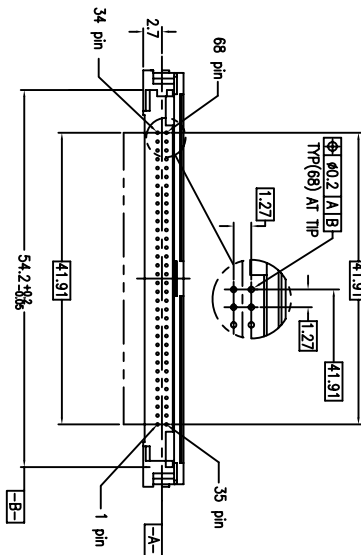
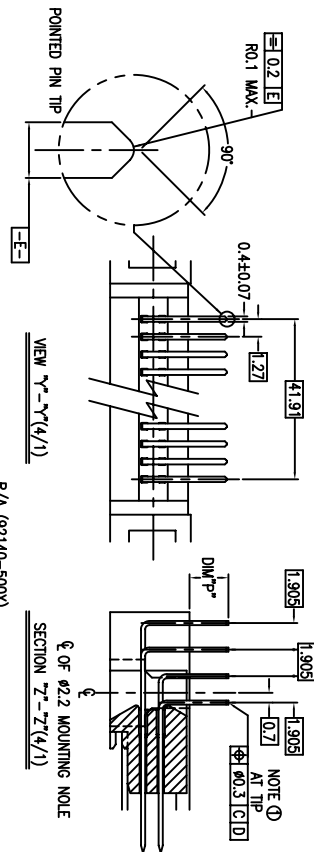
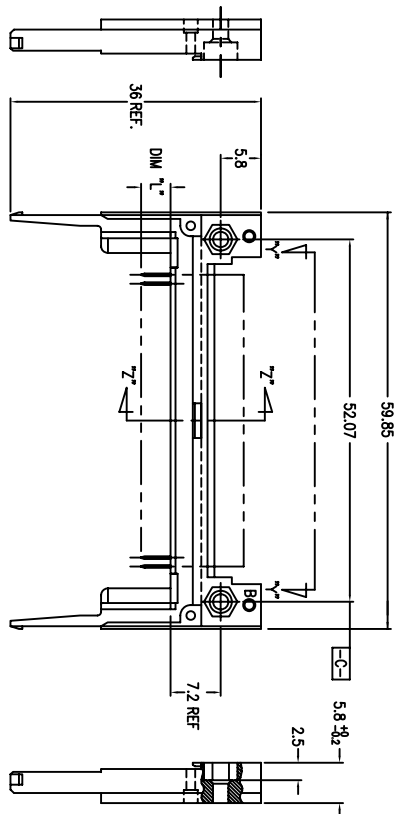
- ① TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
 ② SEE TA-946 FOR PCB LAYOUT.
 ③ MATERIAL
 HOUSING : HIGH TEMPERATURE THERMOPLASTIC UL94V-0 BLACK
 PIN : COPPER ALLOY
 HOLD-DOWN : BRASS
 ④ FINISH
 PIN
 UNDER PLATING : 0.5μm MIN. NI
 CONTACT AREA : 0.076 μm MIN. GOLD
 SOLDER AREA : 2.5mm MIN. Sn-Pb
 OR 2.5mm MIN. PURE Sn.(FOR -XXXXF)
 HOLD-DOWN : 2.5mm MIN. PURE Sn.
 ⑤ RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
 ⑥ SEQUENCE PIN ASSIGNMENT

		DIM 7	
	4.25±0.1	3.5±0.1	5.0±0.1
PIN No.	OTHERS	36.67	117.34 35.51.68

7. GENERAL TOLERANCE : ± 0.3
8. SEE SHEET 1 REGARDING COMPONENTS.
9. THIS PRODUCT MEETS EUROPEAN UNION
DIRECTIVES AND OTHER COUNTRY
REGULATIONS AS DESCRIBED IN GS-22-008

mat'l. code		surface		tolerance		projection		product family		PCMCIA	
ISO 1302		ISO 406, ISO 1001									
litr	ecn no	dfr	date	tolerances unless otherwise specified				title			
			angles	0.1X±0.3		MM		68 POS ELECTOR HEADER ASSY			
			linear	0.1X±0.13		MM		FOR 5.0V APPLICATION			
			±2°	0.1X±0.051		scale		dwg no		sheet 4 of 7	
								9214.0		size A4	
			dfr	Mike Zhou		11/21/06					
			engr	Mike Zhou		11/21/06					
			chr	David Alm		11/21/06					
			appd	Jack Wang		11/21/06					
sheet		revision						type		CUSTOMER	
index		sheet						drawing			

PRODUCT NO.
92140-500
92140-500LF
92140-500H
92140-500HLF
92140-501
92140-501LF
92140-501H
92140-501HLF
92140-502
92140-502LF
92140-502H
92140-502HLF
92140-503
92140-503LF



- NOTES
1. TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
 2. SEE 1A-946 FOR PCB LAYOUT.
 - ③ MATERIAL
HOLISING : HIGH TEMPERATURE THERMOPLASTIC UL94V-0 BLACK
PIN : PHOSPHOR BRONZE
HOLD-DOWN : BRASS
 - ④ FINISH
PIN : UNDER PLATING : 0.5µm MIN. NI.
CONTACT AREA : 0.076 µm MIN. GOLD
- SOLDER AREA : 2.5µm MIN. Sn-Pb.
OR 2.5µm MIN. PURE Sn.(FOR -XXXLF)
HOLD-DOWN : 2.5µm MIN. PURE Sn.
5. RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
6. SEQUENCE PIN ASSIGNMENT
- | PIN No. | DIN 17 | |
|---------|----------|---------|
| | 4.25±0.1 | 5.0±0.1 |
| OTHERS | 3.5±0.1 | 3.0±0.1 |
| | 36.67 | 34.78 |
7. GENERAL TOLERANCE : ±0.3
8. SEE SHEET 1 REGARDING COMPONENTS.
9. THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008

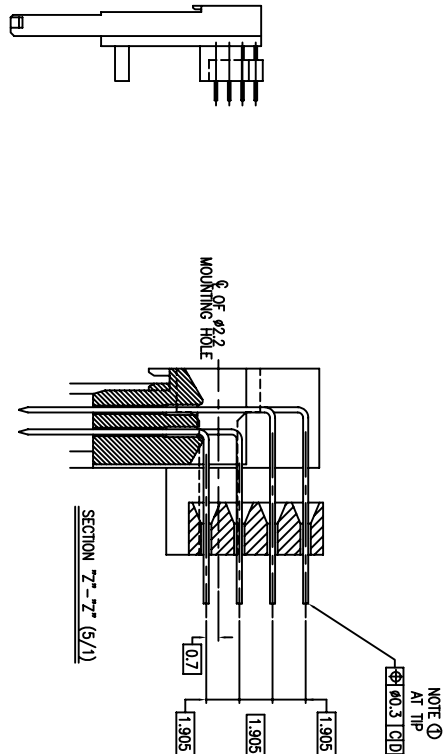
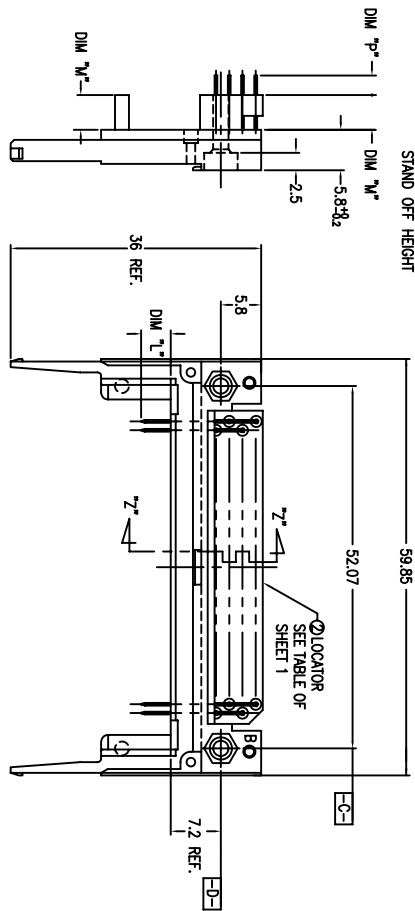
	DIM 1	
	4.25±0.1	5.0±0.1
PIN No.	OTHERS	35.5168

mat'l. code		surface	tolerance	projection	product family	PCMCIA
		ISO 1302	ISO 406 ISO 1001			
litr	ecn no	date	tolerances unless otherwise specified	 MM	title	68 POS EJECTOR HEADER ASSY FOR 5.0V APPLICATION
AW			angles			
			linear	 MM		
			±2°	0.XXX±0.051	scale	
		dr	Mike Zhou	11/21/06	dwg no	sheet 5 of 7 size
		engr	Mike Zhou	11/21/06	92140	A4
		chr	David Qin	11/21/06		
		apprd	Jack Wang	11/21/06		
sheet		revision			type	CUSTOMER
index	sheet				Drawing	

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PRODUCT NO.		
92140-510	92140-510LF	92140-510HLF
92140-520	92140-520LF	
92140-520H	92140-520HLF	
92140-530	92140-530LF	
92140-540	92140-540LF	



NOTES

- ① TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
- ② SEE TA-946 FOR PCB LAYOUT.
- ③ MATERIAL

- HOUSING : HIGH TEMPERATURE THERMOPLASTIC UL94V-0 BLACK
PIN : COPPER ALLOY.

- HOLD-DOWN : BRASS
④ FINISH
PIN

- UNDER PLATING : 0.5 μ m MIN. Ni.
CONTACT AREA : 0.076 μ m MIN. GOLD

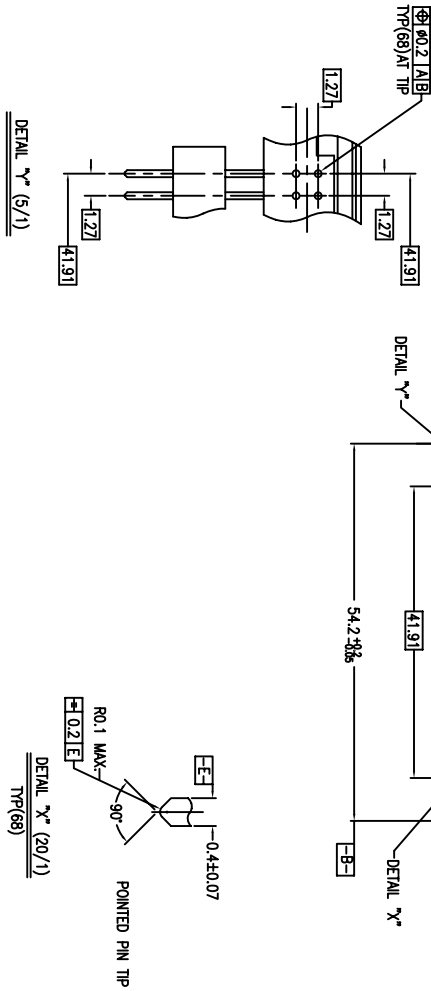
- SOLDER AREA** : 2.5um MIN. Sn-Pb.
OR 2.5um MIN. PURE Sn.(FOR -XXLF)

5. RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3 ± 0.1

- ## 6. SEQUENCE PIN ASSIGNMENT

	DIM. L ₁	
	4.25±0.1	5.0±0.1
PIN No.	OTHERS	36.67
		117.34 35.51.68

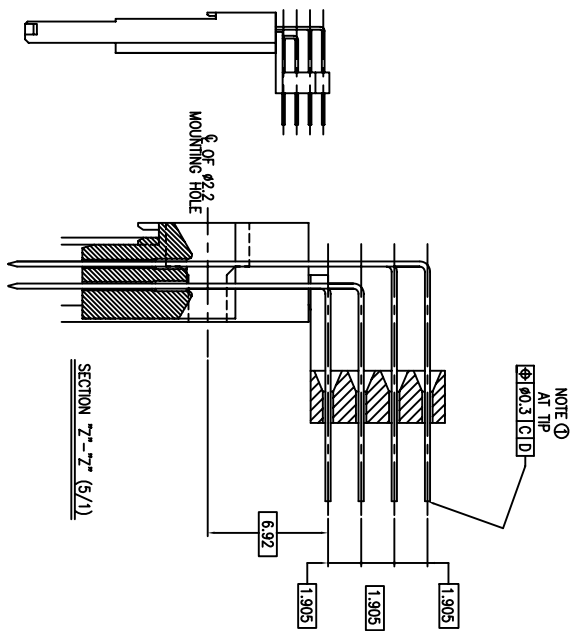
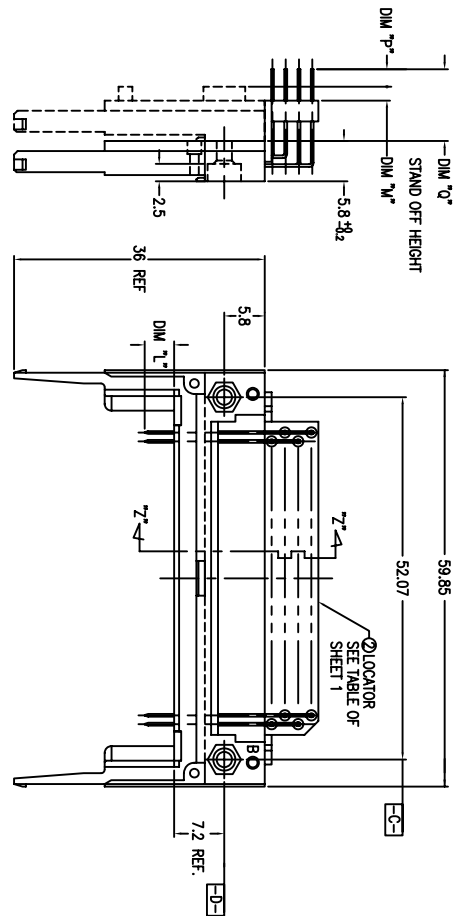
7. GENERAL TOLERANCE : ± 0.3
8. SEE SHEET 1 REGARDING COMPONENTS.
9. THIS PRODUCT MEETS EUROPEAN UNION
DIRECTIVES AND OTHER COUNTRY
REGULATIONS AS DESCRIBED IN GS-22-008



mat'l. code		surface		tolerance		projection		product family		PCMCIA	
ISO 1302		ISO 406		ISO 101							
litr		ecn no		gr		date		tolerances unless otherwise specified		title	
AM		angles		linear		0.0±0.3 0.0XX±0.13		MM		68 POS EJECTOR HEADER ASSY FOR 5.0V APPLICATION	
		±2°		0.0XX±0.051		scale				dwg no	
		gr		Mike Zhou		11/21/06				sheet 6 of 7	
		engr		Mike Zhou		11/21/06				size A4	
		chr		David Qin		11/21/06					
		dppd		Jack Wang		11/21/06					
sheet		revision									
index		sheet									

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PRODUCT NO.
92140-505
92140-505LF
92140-535
92140-535LF
92140-545
92140-545LF



NOTES

- ① TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
2. SEE TA-946 FOR PCB LAYOUT.

© MAIL

- HOUSING : HIGH TEMPERATURE THERMOPLASTIC UL94V-0 BLACK
PIN : COPPER ALLOY.
WELD POINT : BRASS

- ④ HOLD-DOWN : BRASS
FINISH

7

- CONTACT AREA : 0.076 μm MIN. GOLD

- SOLDER AREA : 2.50um MIN. Sn-Pb.**
OR 2.5um MIN PLATE Cu (ENP - YW1E)

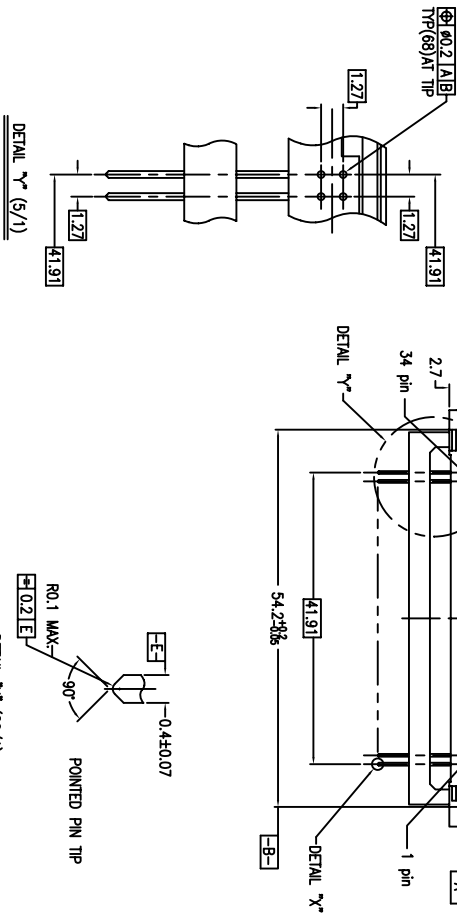
- ON 2.5011 MIN. FORK SL.(FOR -AALE)
HOLD-DOWN : 2.50u MIN. PURE Sn.



- 5. RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1**

- ## 6. SEQUENCE PIN ASSIGNMENT

		DIM "L"	
		4.25±0.1	5.0±0.1
PIN No.	OTHERS	36.67	11734 35.51,68

7. GENERAL TOLERANCE : 40.3
8. SEE SHEET 1 REGARDING COMPONENTS.
9. THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
10. PRODUCT SPEC: 110-263



mat'l. code		surface	tolerance		projection 	product family	PCMCIA	
		ISO 1902	ISO 406	ISO 1101				
lit	ecn no	dr	date	tolerances unless otherwise specified		title	68 POS EJECTOR HEADER ASSY FOR 5.0V APPLICATION	
AM				angles	$0.X \pm 0.3$			
				linear	$0.XX \pm 0.13$	MM 		
				± 27	$0.XXX \pm 0.051$			
					scale	dwg no	sheet 7 of 7 size	
				dr	Mike Zhou	9214.0	A4	
				engr	Mike Zhou			
				chr	David Qin			
				apprd	Jack Wang			
sheet	revision							
index	sheet							